POLSINELLI

Simon G. Booth

PRINCIPAL

San Francisco, CA | 202.772.1498
sbooth@polsinelli.com

Simon Booth combines his foundation as an electrical engineer with hands-on experience in cutting-edge AI and patent law to help clients protect and maximize the value of their innovations. Drawing on deep technical experience from semiconductors to modern transformer pipelines, quantization techniques and applied machine learning, he partners with inventors to develop a granular understanding of the underlying technology. Simon works with startups, established companies and investors to build strategic patent portfolios, identify high-value assets and align intellectual property strategy with business goals.

Simon works closely with trial teams in patent litigation matters to develop strategy and technical positions. He also represents clients involved in licensing and due diligence related to investments and other transactions.

Bringing a dual strength in law and technology, Simon develops custom software to support the firm's IP practice and its clients. He is the lead developer of Polsinelli PatentCAD — an artificial intelligence tool for drafting patent applications — and is the first named inventor on multiple patent applications based on his innovations in legal technology.

That blend of legal and technical fluency is grounded in his earlier career as an RF Design Engineer, where he developed gallium nitride (GaN) power transistors at an emerging growth semiconductor company. He also worked for a major mobile telecommunications company in hardware and software engineering prior to entering private practice.

Simon is involved primarily in patent portfolio management for a variety of client types and is most active in:

- Artificial intelligence and natural language processing
- Display technologies, circuits and manufacturing (OLED, Micro-LED, LCD)
- Semiconductor devices, equipment and manufacturing
- RF and microwave circuits
- Wireless communications (5G, LTE, LTE-A, 802.16)
- Analog and mixed signal circuits, amplifiers and synthesizers
- Cloud computing and networking

Capabilities

- Intellectual Property
- Patent Preparation & Prosecution
- Electrical Engineering & Computer Science Patent Prosecution
- International Intellectual Property
- In-House Automated Patent Drafting Tool
- International Patent Filings & Strategy
- Artificial Intelligence & Machine Learning

- Operating systems
- Signal processing
- Clean energy

Not admitted in California – practice in California limited to Federal Law.

Education

- University of Illinois Chicago School of Law (J.D., 2007)
- North Carolina State University (B.S., 2000)
    - Electrical Engineering

Bar Admissions

- District of Columbia, 2012
- Admitted to practice before the United States Patent and Trademark Office, 2006

Court Admissions

- U.S. District Court, Northern District of Illinois
- United States Court of Appeals for the Federal Circuit

Matters

- Represented an international manufacturer of electrical and electronic products in a patent infringement lawsuit and procured a favorable claim construction that resulted in opposing counsel conceding infringement on summary judgment.

Publications

November 17, 2025
Integrating AI into Patent Practice: Tools, Compliance, and Workflow Optimization
Co-Author, Thomson Reuters